

TFLN PIC PLATFORM

UNLEASHING MONOLITHIC POWER TO ENHANCE HYBRID/HETEROGENEOUS PICS

CSEM AT A GLANCE

We are a public-private,
non-profit, Swiss
technology innovation center

We enable competitiveness
through innovation by
developing and transferring
world-class technologies to
industry



230
INDUSTRIAL
CLIENTS / YEAR



203
PATENT
FAMILIES

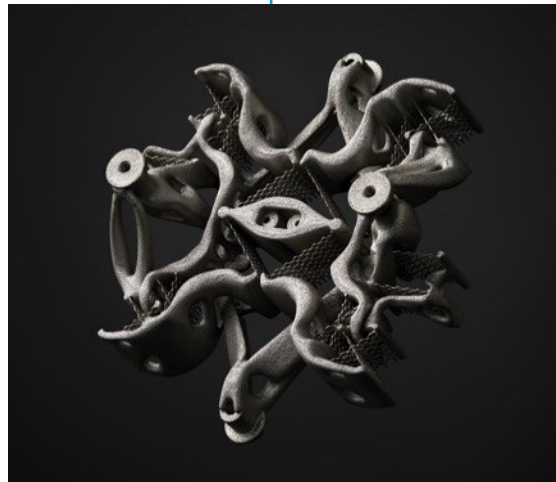
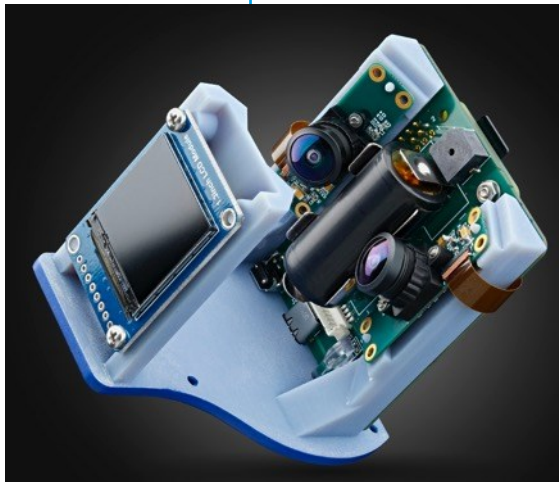
OUR DNA COMES FROM OUR WATCHMAKING ROOTS

Small & precise

Complex

Ultra-low-power

Multidisciplinary



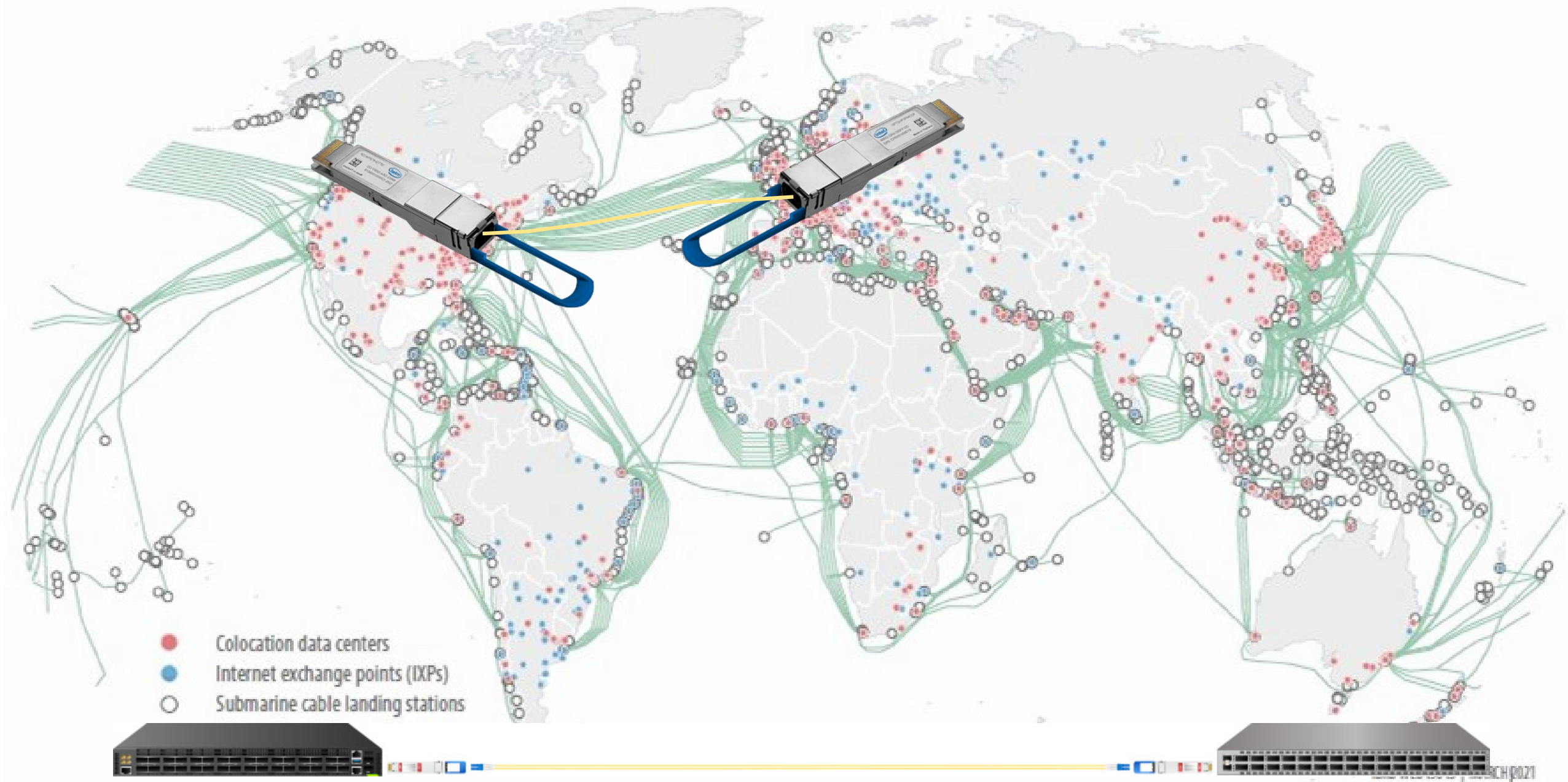
THREADS

Why PICs? Why TFLN PICs?

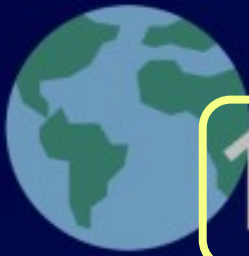
TFLN PICs Portfolio at CSEM



Facebook Map of all the World's Interconnected Friendships



8 DATA CENTER FACTS YOU MIGHT NOT KNOW



1 There are over **8,000** Data Centers Worldwide

2 Over **33%** of the world's Data Centers are based in the **USA**



3 On average, one Data Center **consumes** as much energy as **25,000** homes

4 Data Centers generate **1,670** new jobs and **\$77.7M** in earnings a year on average

5 Data Center equipment is renewed every **3 to 5** years

8 The **Uspenski Orthodox Cathedral** in Helsinki, Finland, is now the location of a **Data Center**, while historically being a bomb shelter during World War II

7 The world's **largest** Data Center, owned by **China Telecom**, is over **10M** square feet in area

6 In a typical Data Center, only **40%** of energy consumption goes towards **computing**. The **rest** is used mostly for **cooling down the servers**



Photonic Integrated Circuit

- ✓ Complex systems
- ✓ Enhanced performance
- ✓ Novel functionalities
- ✓ **Low power**
- ✓ **Low cost**
- ✓ **Scalable**



✓ Miniaturization

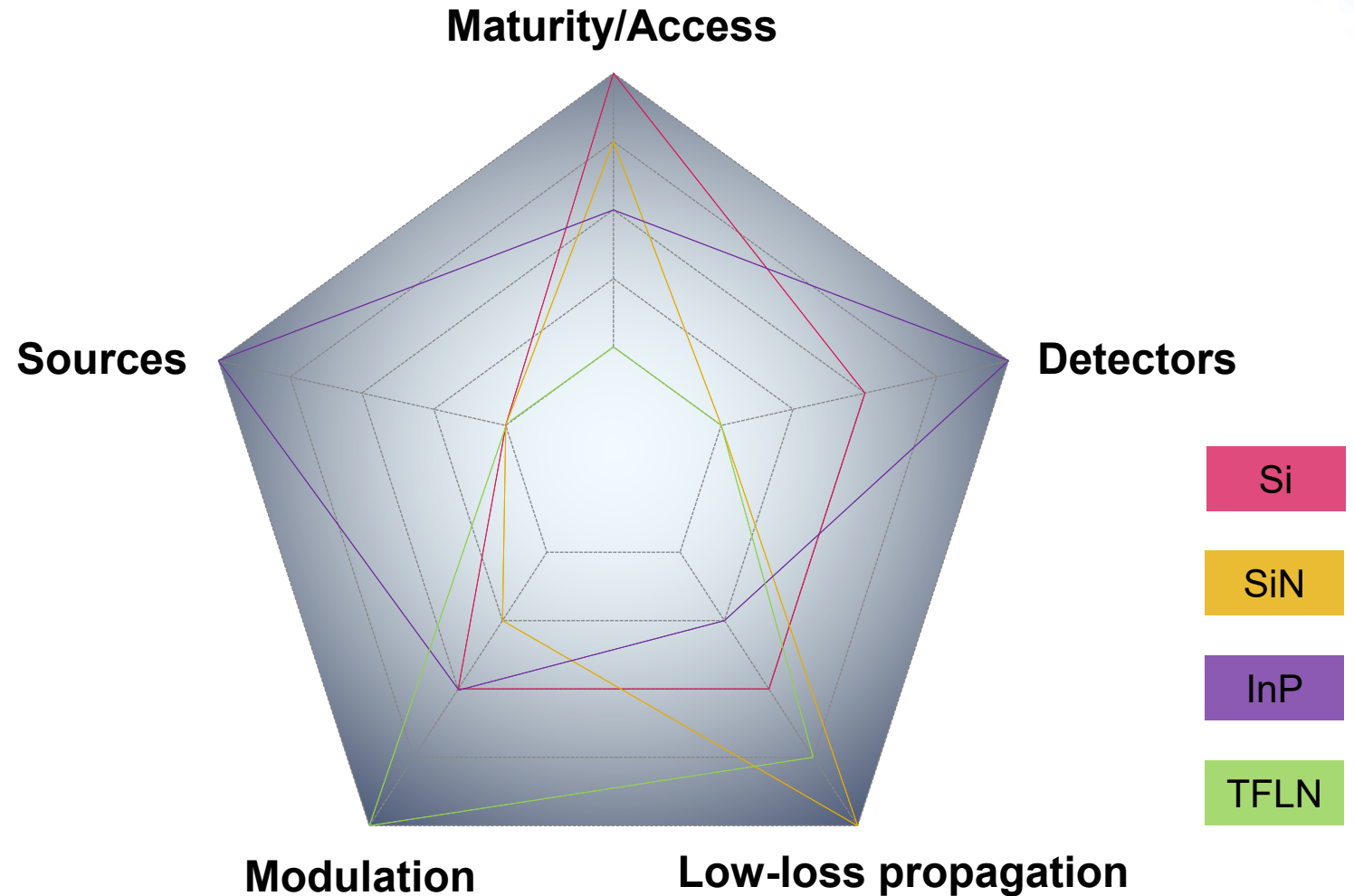


ARE PICS READY TO ADDRESS THE NEEDS?!

Need for Components to **Generate**, **Manipulate**, **Transport**, and **Detect** light
No single material can do everything!

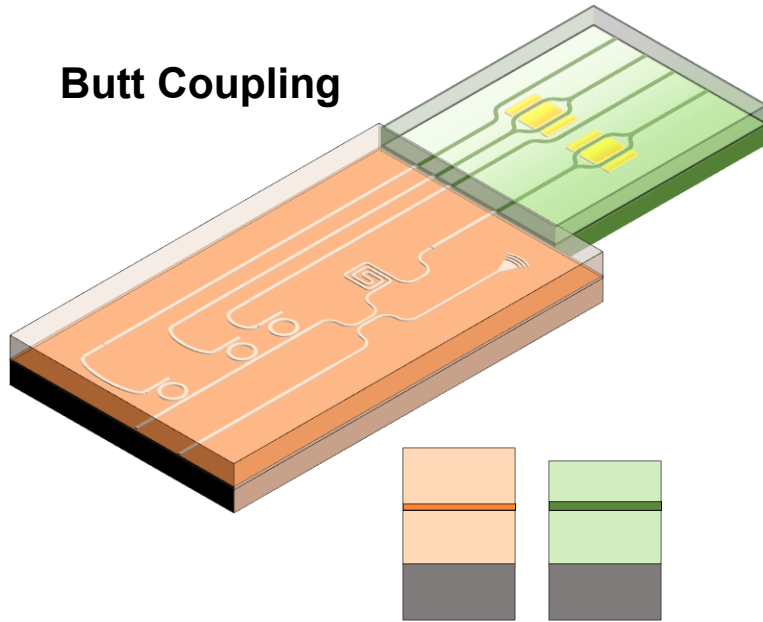
PIC Systems

- Optical interfacing
- CMOS integration
- Power consumption
- Form factor

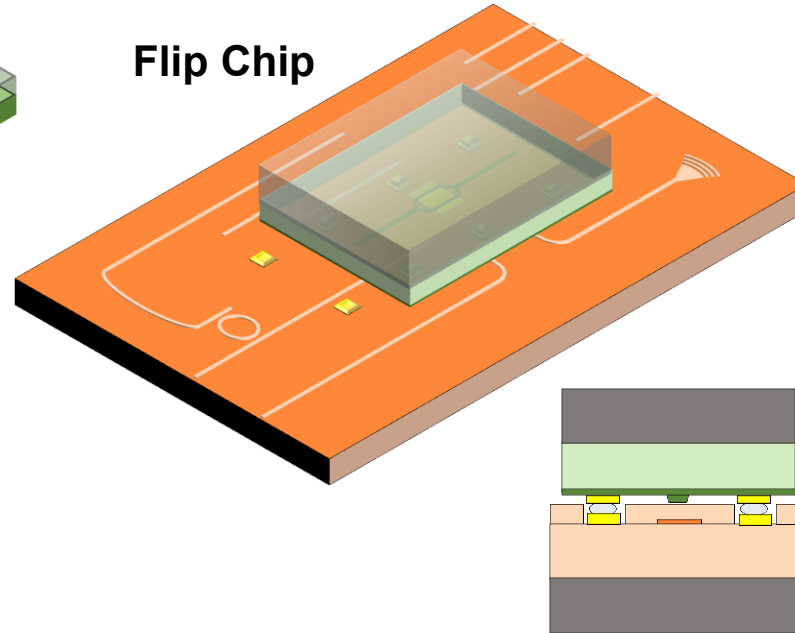


ADVANCED PICS OFFERINGS: HETEROGENEOUS/HYBRID INTEGRATION

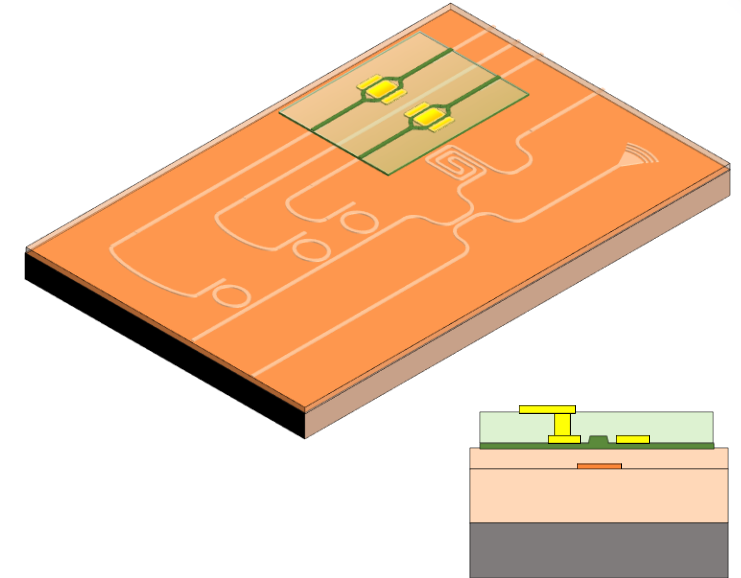
Butt Coupling



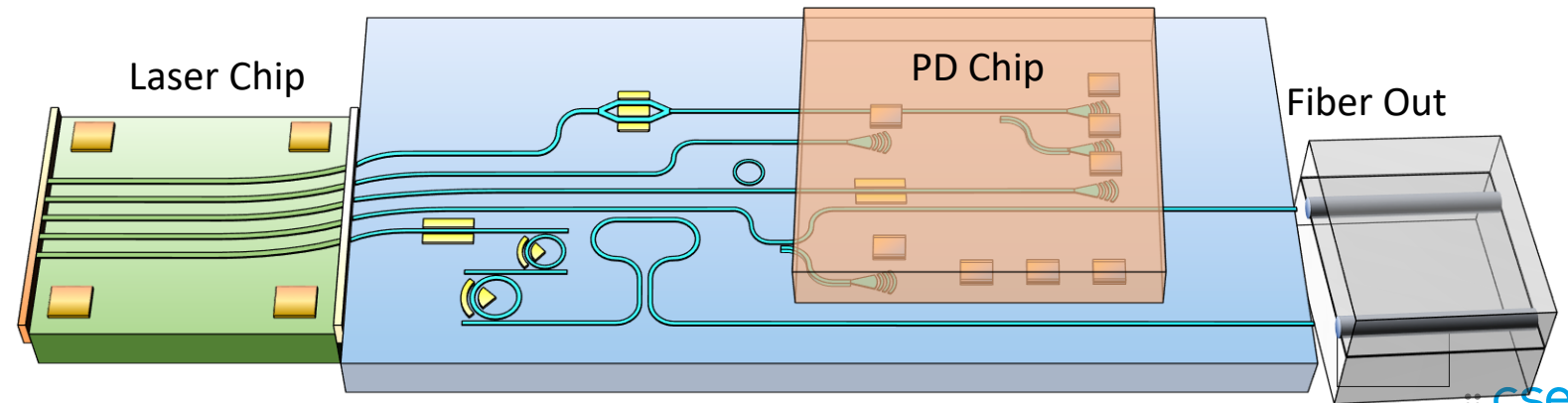
Flip Chip



Micro-Transfer Printing



- Alignment
- In/Out coupling efficiency
- Scalability/Process compatibility
- Electronics integration



AN ENABLING TECHNOLOGY

Thin-film lithium niobate (TFLN) on insulator

Intrinsic nonlinearity

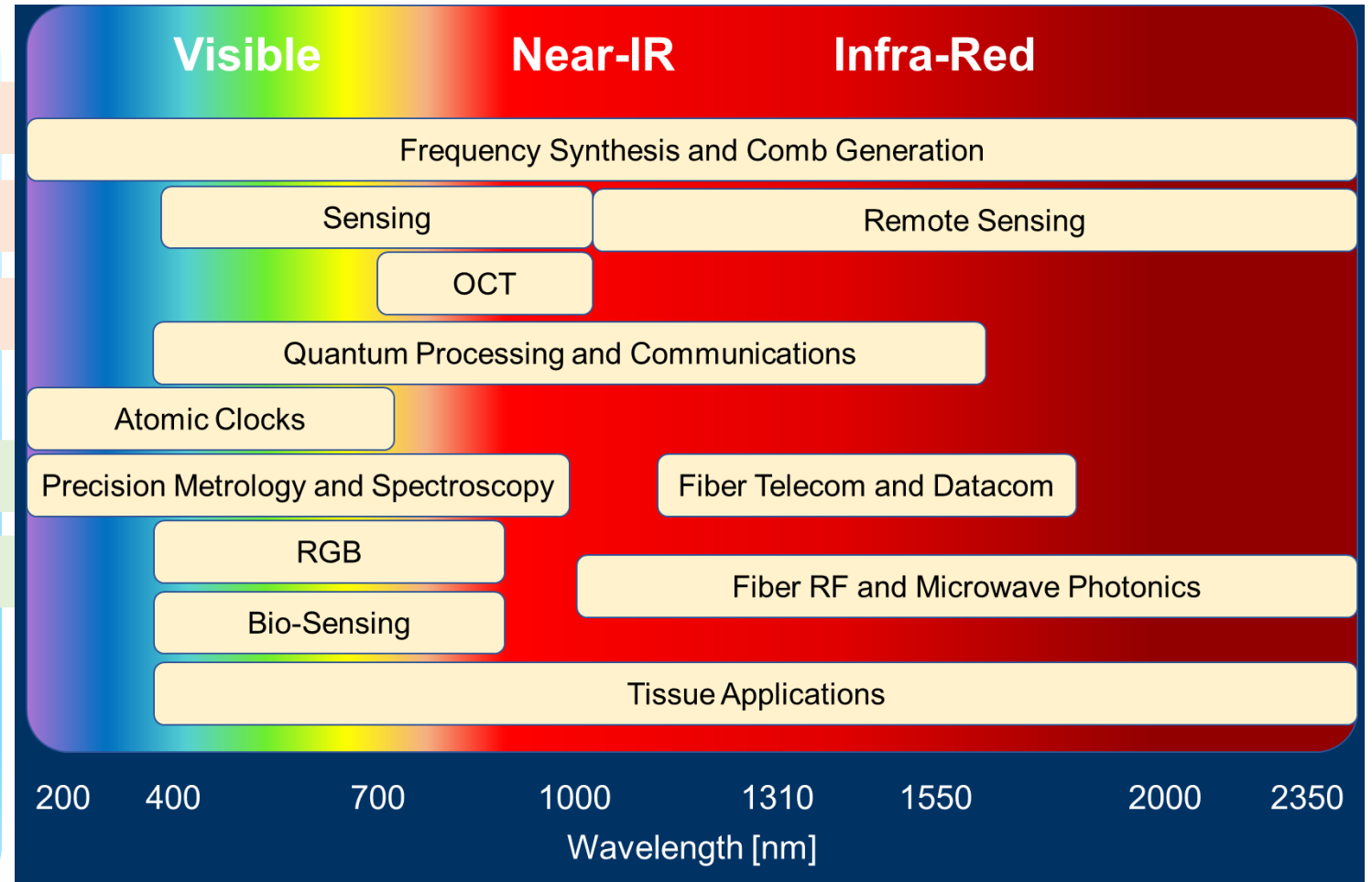
Wide Transparency Window

High Optical Power Handling

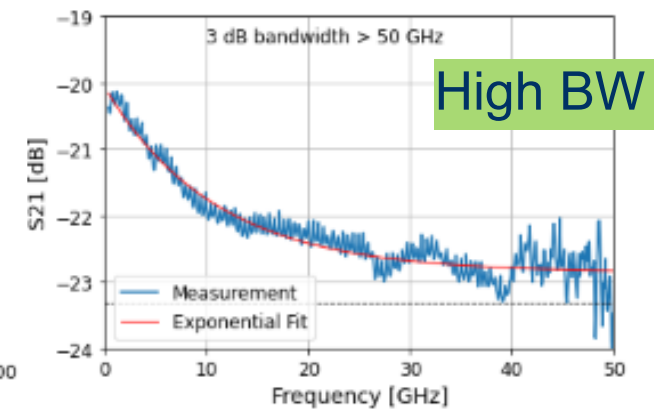
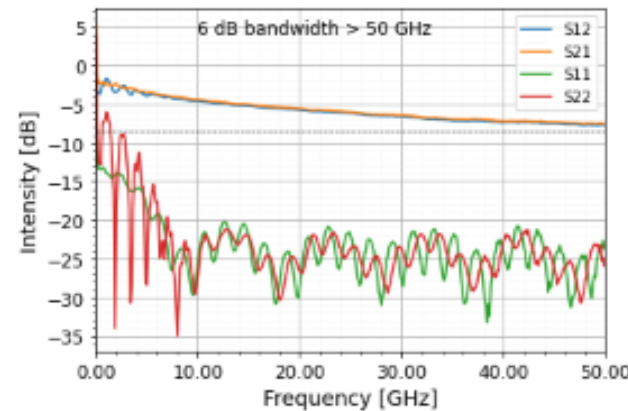
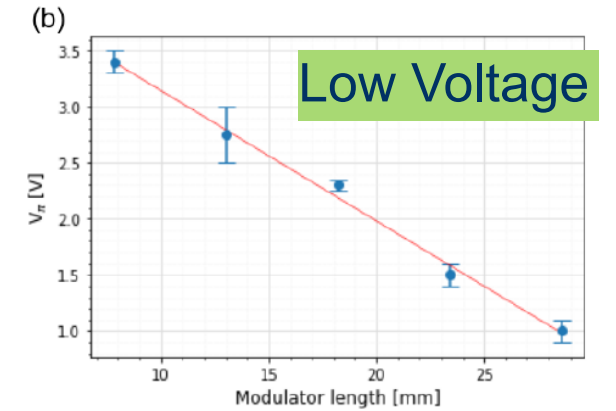
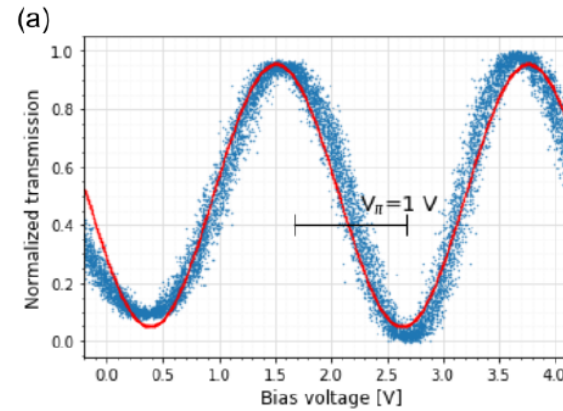
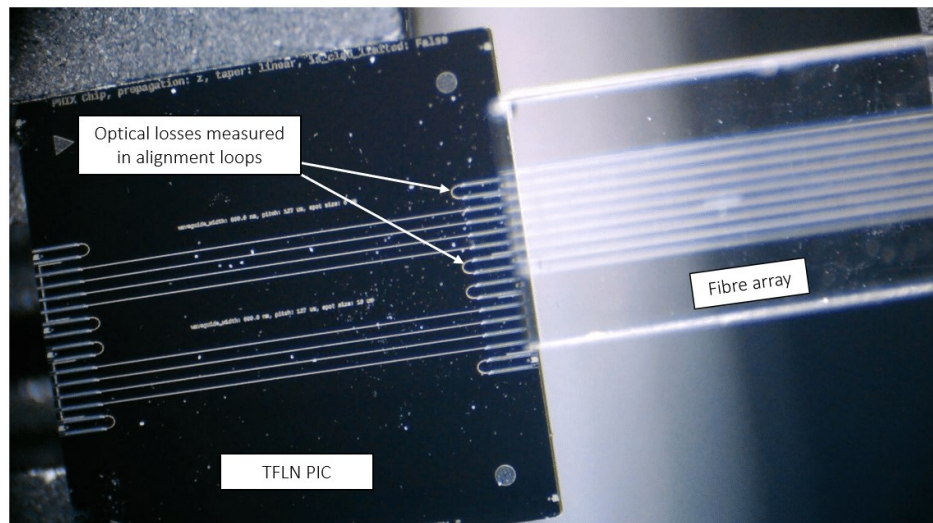
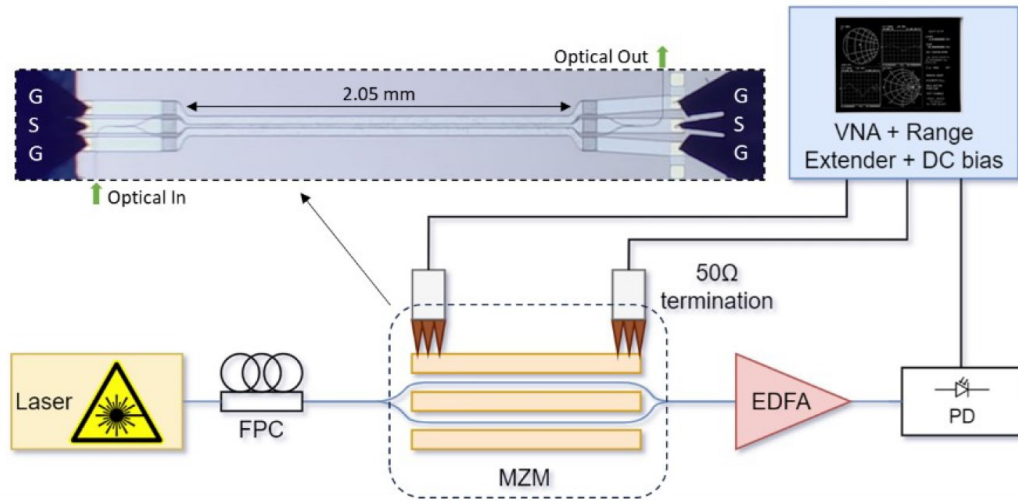
Fast EO Modulation

Frequency Conversion

Miniaturization



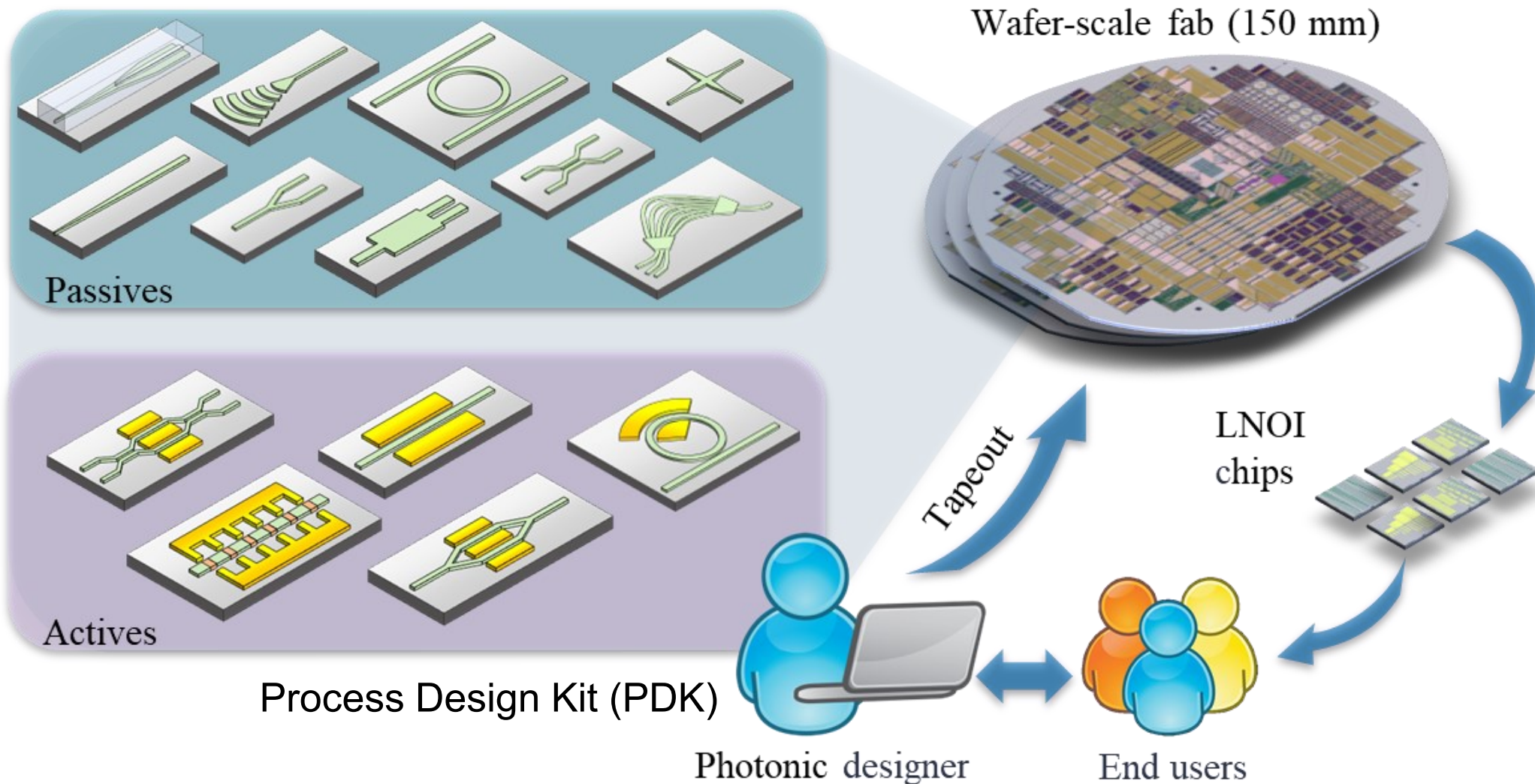
CSEM TFLN PIC PLATFORM TECHNOLOGY: HIGHLIGHTS



Fiber-to-chip optical coupling losses of less than 2.0 dB per facet, demonstrated by PHIX

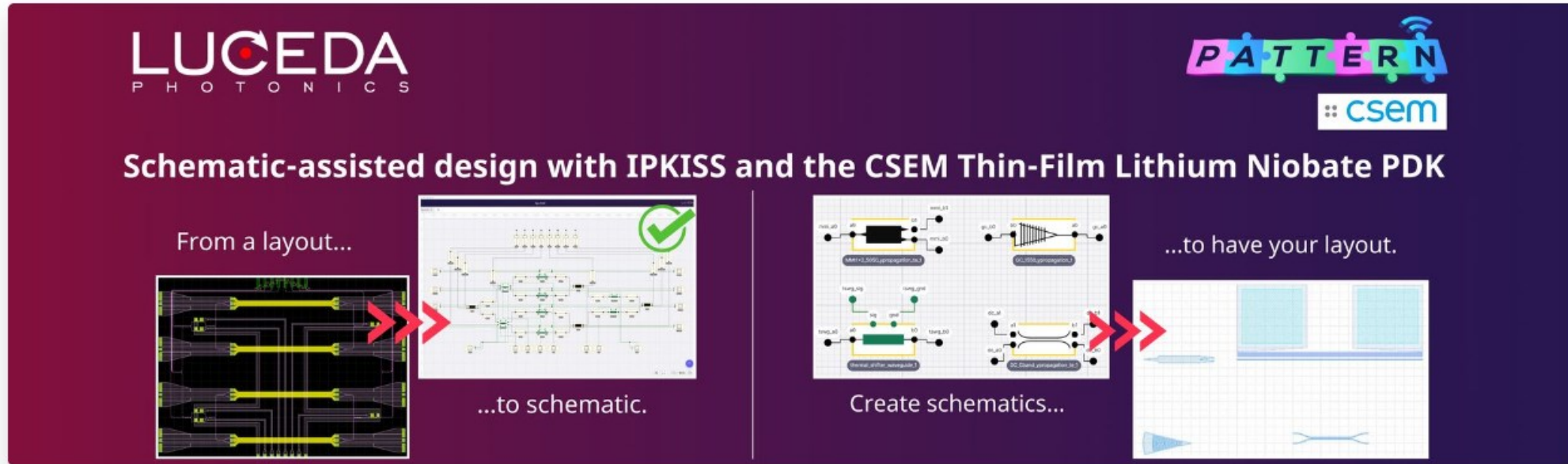
<https://pattern-project.eu/key-milestone-achieved-for-facet-fiber-chip-coupling/>

CSEM TFLN PIC PLATFORM: OPEN-ACCESS

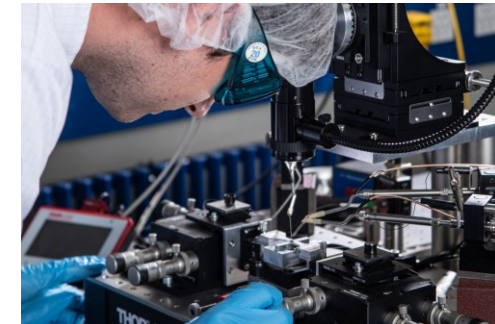
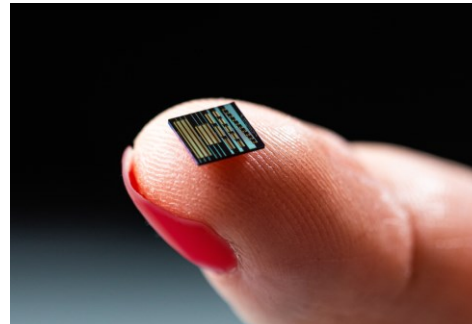
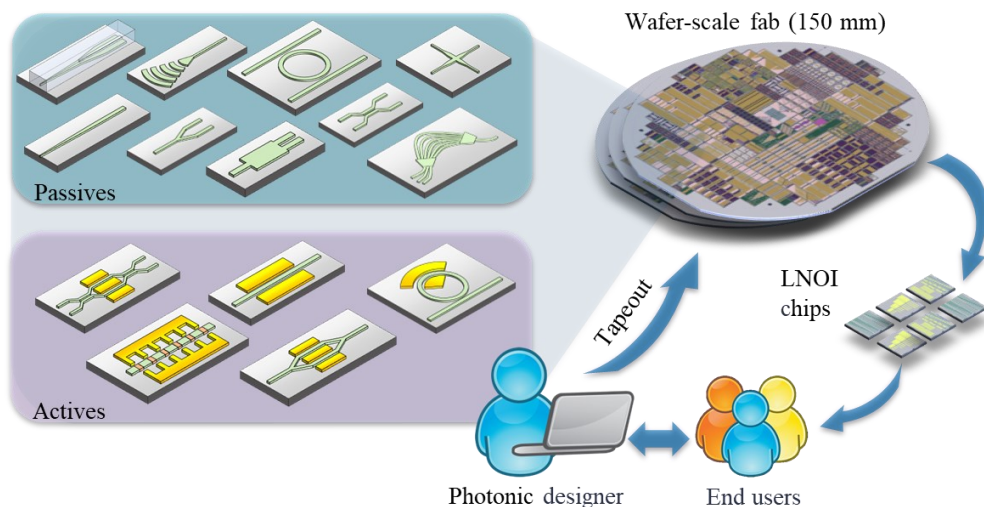


CSEM TFLN PIC PLATFORM: OPEN-ACCESS

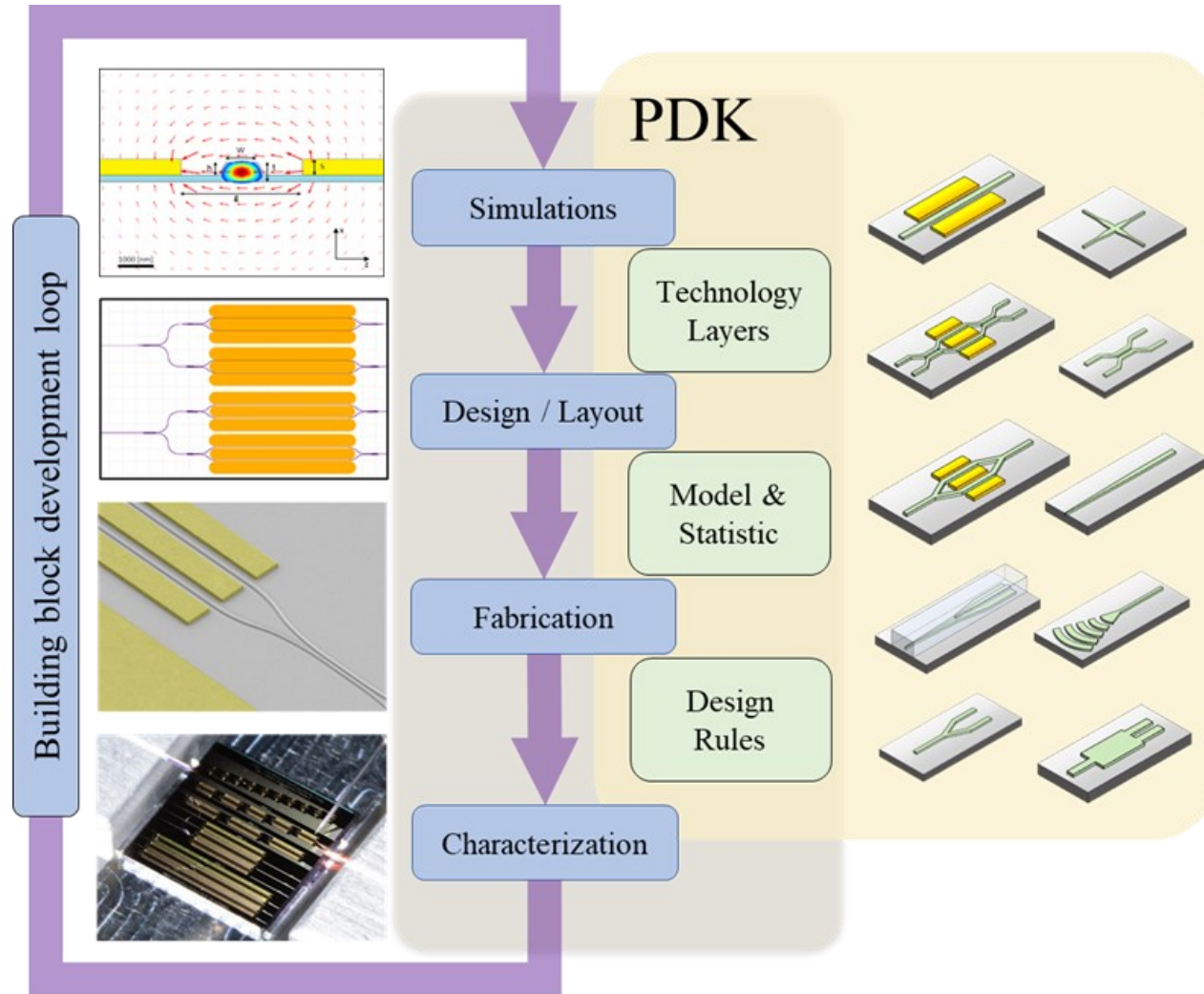
🕒 13 December, 2023 by 🌐 Luceda Photonics



<https://www.lucedaphotonics.com/blog/news-6/pattern-schematic-assisted-design-with-luceda-ipkiss-and-the-csem-thin-film-lithium-niobate-tfln-pdk-74>



TOWARDS STANDARDIZATION



C-Band Building Blocks (version 2023)

Building Block	Performance
Waveguide	Propagation loss: 1 dB/cm single mode, < 0.2 dB/cm multimode
MMI (1x2, 2x2)	Insertion loss < 0.2 dB, 3dB BW > 60nm
Directional Coupler	Insertion loss < 0.2 dB, 3dB BW > 30nm
Waveguide Crossing	Insertion loss < 0.1 dB, Crosstalk < 30 dB
Grating Couplers	Coupling loss: 8 dB, 3dB BW > 30 nm
Edge couplers	Coupling loss: 3 dB, 3dB BW > 70 nm
EO modulaors	$V_{\pi}L < 3 \text{ V.cm}$, BW > 45 GHz

✓ Pcell for customized components

In the pipeline in 2024:

- Thermal phase shifters
- Polarization controllers
- PPLN for SHG
- 780 nm PDK

FROM PLATFORM STANDARDIZATION TO THE APPLICATION

TFLN Foundry



LIDAR



Telecom



Quantum Technology



Standardization

- Monolithic TFLN - PDK
- Hybrid and Heterogeneous
- CMOS integration
- Packaging - ADK

Application oriented

- LIDAR
- Telecom
- Quantum
- Sensing

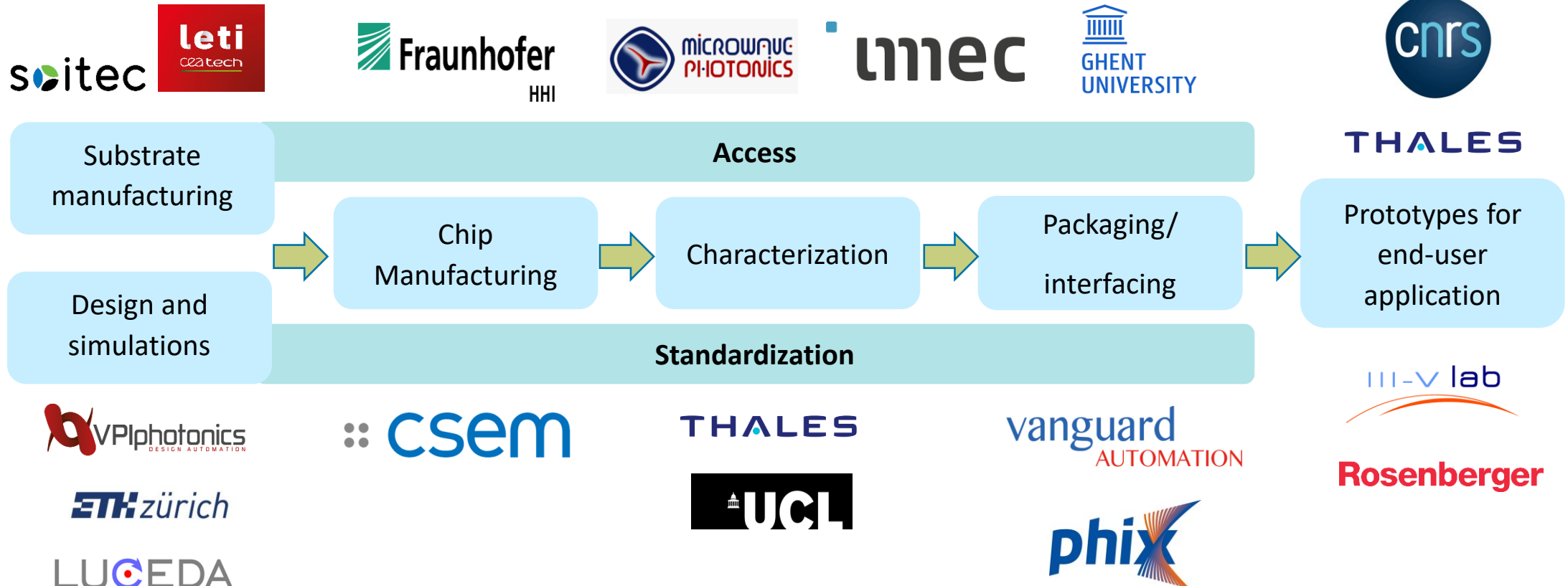
STANDARDIZATION; TFLN PICS VALUE CHAIN



- LNOI PIC Foundry, PDK
- Supply chain for TFLN PICs



- Hybrid/Heterogeneous integration
- ADK
- Supply chain for hybrid PICs

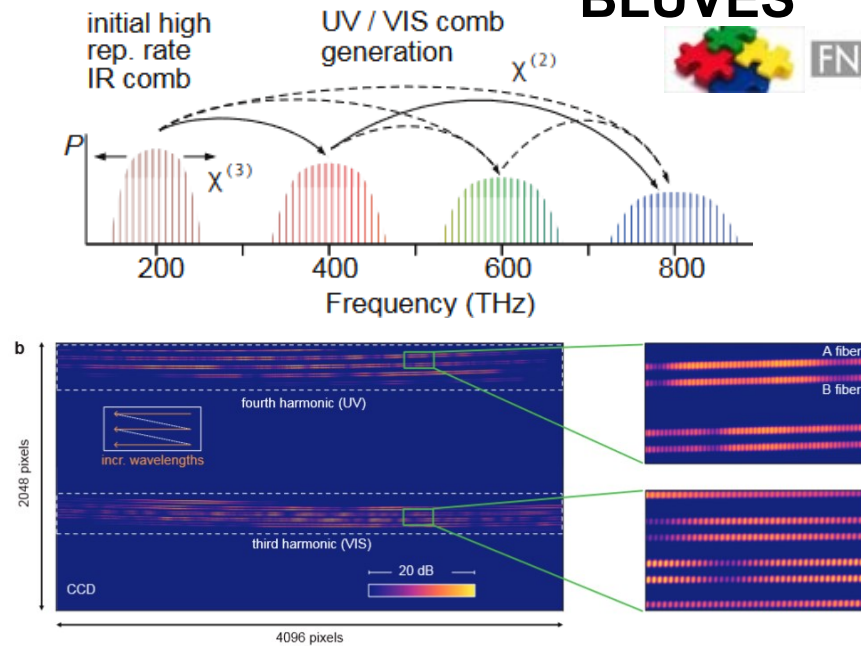


FROM PLATFORM STANDARDIZATION TO THE APPLICATION

Astronomical calibration

Electro-optic comb frequency conversion to the Vis/UV

BLUVES



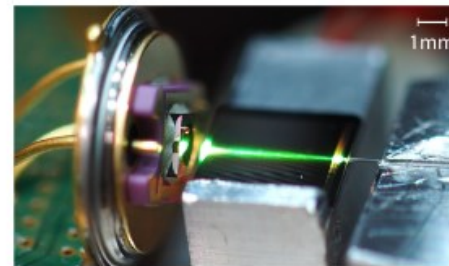
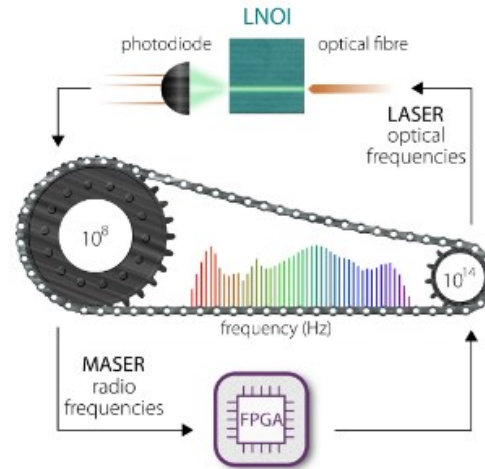
Ludwig, M. *et al.* arXiv:2306.13609 (2023)

BLUVES



Laser Metrology

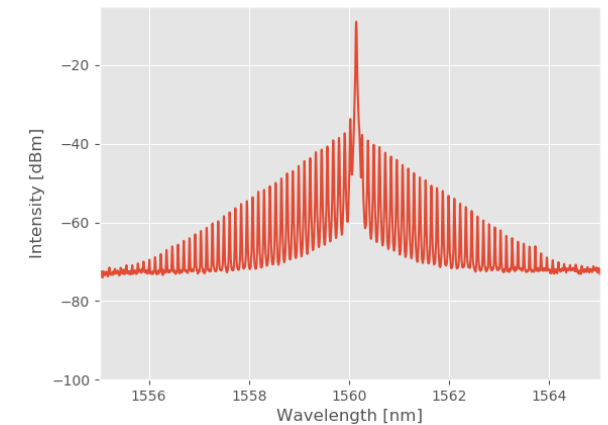
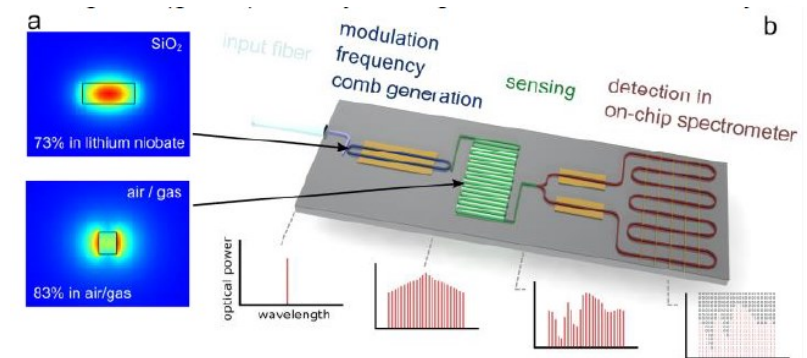
Laser stabilization



Obrzud, E. *et al.* APL Photonics 6, 121303 (2021)

Gas spectroscopy

Electro-optic comb + spectrometer

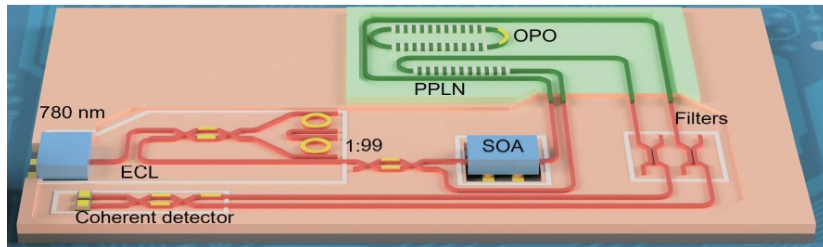


LINIOS
BRIDGE FNSNF

FROM PLATFORM STANDARDIZATION TO THE APPLICATION

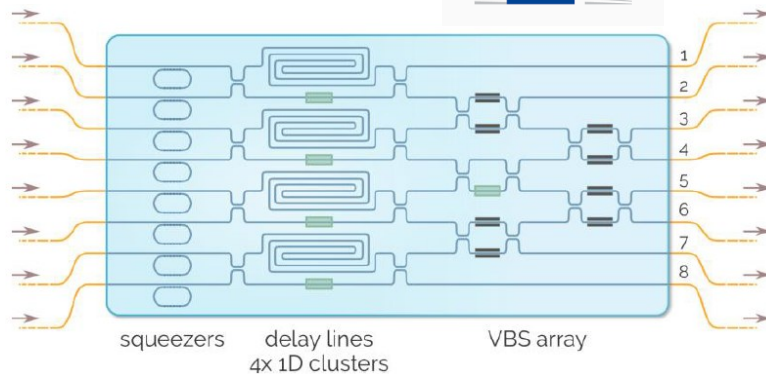
Quantum

QKD transmitter
(Integration with SiN, Ge, GaAs)



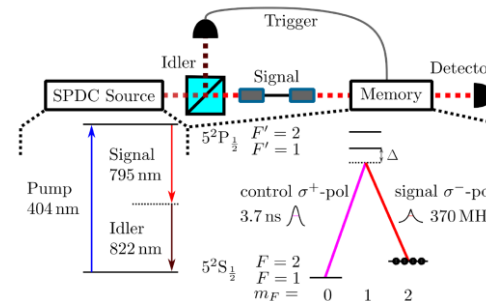
Photonic Quantum
Computing

CLUSTEC



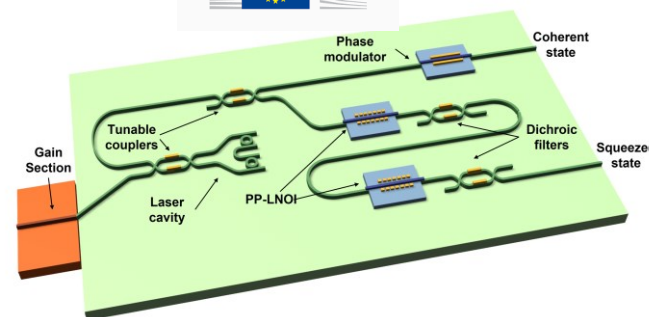
Single photon
frequency conversion

sQnet



Squeezed state laser
(Integration with SiN, GaAs)

QUANTIFY



LIDAR

On-chip FMCW LIDAR module
(Integration with SiN, InP, Ge)

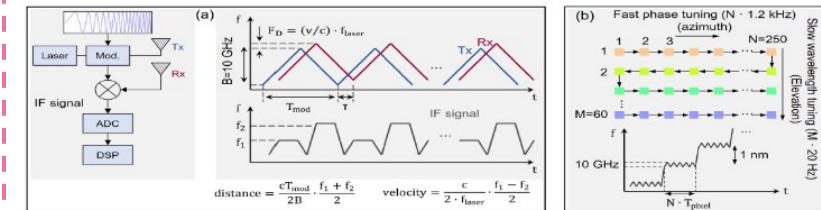
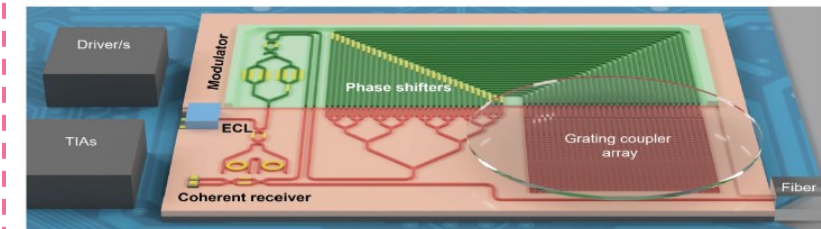


Figure 6: (a) Fundamentals of FMCW LIDAR operation. (b) Synchronization scheme for simultaneous 2D scanning and FMCW operation.



TAKEAWAY MESSAGE

- Monolithic PIC platform development is essential for advanced hybrid/heterogeneous PICs
- Not every application requires hybrid-heterogeneous PICs
- TFLN brings huge added value to the PIC portfolio: monolithic and heterogeneous



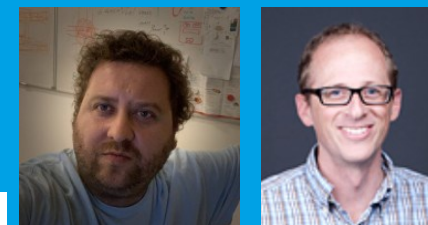
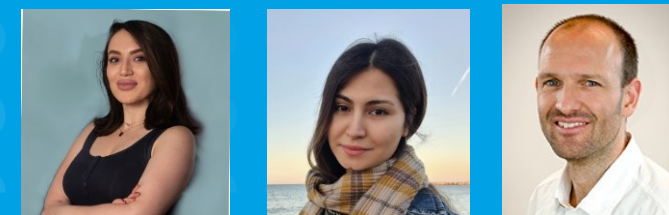
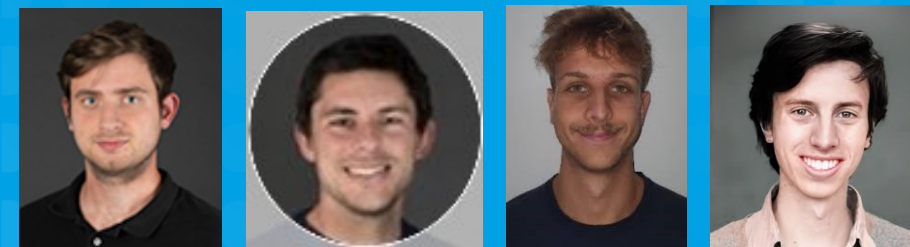
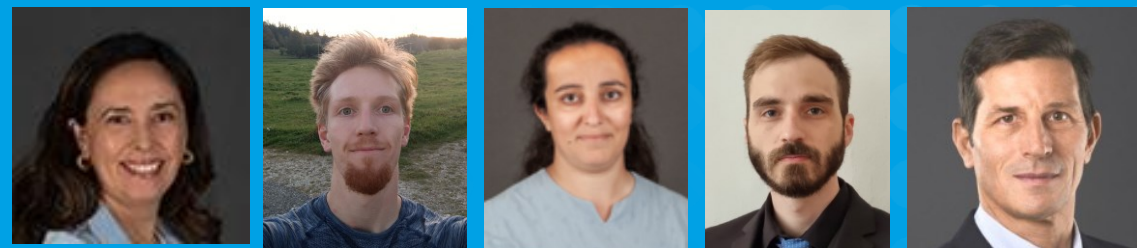
Focus Area

- Chip manufacturing
- Packaging
- Hybrid/Heterogeneous Integration
- Enhancing the supply chain

Challenges

- Optical interfacing
- CMOS integration
- Power consumption
- Standardization

FACING A CHALLENGE? LET'S TACKLE IT TOGETHER!



A big Thanks to the TFLN PIC team at CSEM



FACING THE CHALLENGES OF OUR TIME